**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Kalyana Chakravarthy et al.

Filed : October 30, 2003

For : METHOD AND APPRATUS TO REDUCE ACCESS TIME IN

SYNCHRONOUS FIFOS WITH ZERO LATENCY OVERHEAD

Docket No. : 852463.405

Date : October 30, 2003

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **INFORMATION DISCLOSURE STATEMENT**

## Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached form PTO-1449. Applicants note that since the application filing date is after June 30, 2003, copies of the cited U.S. patents and published patent applications are not required and accordingly have not been provided. As to any reference cited, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Director is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Kalyana Chakravarthy et al.

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Form PTO-1449

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Sheet <u>1</u> of <u>1</u>

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			U.S.	PATENT	DOCUMENTS					
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